

IN THE UNITED STATES PATENT AND TRADEMARK OFFICEApplicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/328,910

Filed: 9 June 1999

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2851

Examiner: J. Clark

TECHNOLOGY CENTER 2800

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Lacey**SUPPLEMENTAL AMENDMENT AND SURRENDER OF PATENT CERTIFICATE**Honorable Commissioner for Patents
Washington, D.C. 20231

4 January 2002

Sir:

Supplemental to the Amendment filed 9 November 2001 in response to the restarted Office Action mailed 11 June 2001 in connection with the above-identified application, and in response to a telephone request received 4 January 2002 from the Examiner, Applicant respectfully submits the following.

IN THE SPECIFICATION:

Please delete the section in Column 1 at lines 5-7 (between the title and the heading "BACKGROUND OF THE INVENTION") in its entirety, and enter the following replacement section therefor.

--CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a reissue of U.S. Serial No. 08/311,021 filed 22 September 1994, now U.S. Patent No. 5,637,913, which is a divisional of U.S. Serial No. 08/038,684 filed 29 March 1993, now U.S. Patent No. 5,378,656.

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